

Application No.	Applicant(s)	
10/717,778	LOKE ET AL.	
Examiner	Art Unit	
Dac V. Ha	2611	
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<ul><li>6. ☐ Interview Son Paper No./</li><li>7. ☐ Examiner's</li><li>8. ☒ Examiner's</li></ul>	ummary (PTO-413), Mail Date Amendment/Comment Statement of Reasons for Al	
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## Allowable Subject Matter

1. This office action is in response to the amendment filed on 05/03/07.

2. The following is a statement of reasons for the indication of allowable subject matter:

Applicant has amended claims 23-25 in accordance with the office dated 02/07/07. Upon further consideration, claims 23-25 are allowed. Claims 1-22 were previously allowed. Particularly, prior art of record (closest reference, Gomez - US 6,583,675), taking individually or collectively, fails to fairly teach method and apparatus of phase-locked loop including "a charge-pump loop filter configured to receive a first and a second update signal each having at least one state based on a phase difference between a first clock and a second clock, and comprising: a first component configured to adjust, during an update period, a voltage across an impedance from a reference level based on the states of the first and second update signals and to return the voltage across the impedance to the reference level prior to an end of the update period, wherein the voltage across the impedance comprises the first control voltage; and a second component configured to adjust a voltage across a capacitor based on the states of the first and second input signals, wherein the voltage across the capacitor comprises the second control voltage", as claimed in independent claim 1 (claims 2-16 depend therefrom); "charge-pump loop filter configured to receive a first and a second update signal each having at least one state based on a phase difference between a first clock and a second clock, the charge-pump loop filter comprising: a first component configured to adjust, during an update period, a voltage across a first capacitor from a

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reference level based on the states of the first and second update signals and to return the voltage across the first capacitor to the reference level prior to an end of the update period, wherein the voltage across the first capacitor comprises the first control voltage; and a second component configured to adjust a voltage across a second capacitor based on the states of the first and second input signals, wherein the voltage across the second capacitor comprises the second control voltage", as claimed in independent claim 17 (claims 18-22 depend therefrom); and "receiving a first and second update signal each having at least one state based on a phase difference between a first clock and a second clock; providing the first control voltage having a level substantially proportional to the phase difference, including adjusting, during an update period, a voltage across an impedance from a reference level based on the states of the first and second update signals and to return the voltage across the impedance to the reference level prior to an end of the update period, wherein the voltage across the impedance comprises the first control voltage; and providing the second control voltage having a level substantially proportional to an integral of the phase difference clock", as claimed in independent claim 23 (claims 24-25 depend therefrom). Thus, claims 1-25 are found to be novel and unobvious over prior art of record.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dac V. Ha whose telephone number is 571-272-3040. The examiner can normally be reached on 5/4.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jay Patel can be reached on 571-272-2988. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Dac V. Ha Primary Examiner

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